Deterministic Min-cut in Poly-logarithmic Max-flows

Jason Li

Joint work with Debmalya Panigrahi (Duke)

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Max-flow min-cut theorem: s-t min-cut = s-t max-flow, can recover s-t min-cut given s-t max-flow

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- [This work]: deterministic min-cut for weighted graphs in O(m¹⁺²) time plus polylog(n) calls to s-t max-flow

Our Approach

Main insights: local algorithms and expander decomposition

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"Modern" approach to algorithm design

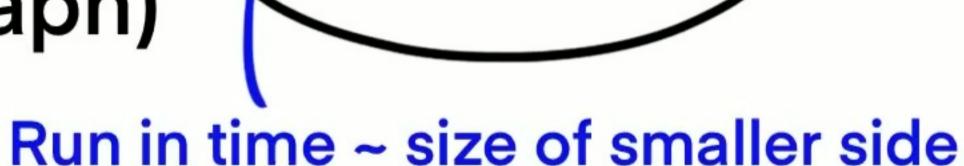
Local Graph Cut Algorithms

Fix a <u>seed</u> vertex s. If there exists a good cut "local" to s, then output in <u>sublinear</u> time (without looking at the whole graph)

Run in time ~ size of smaller side

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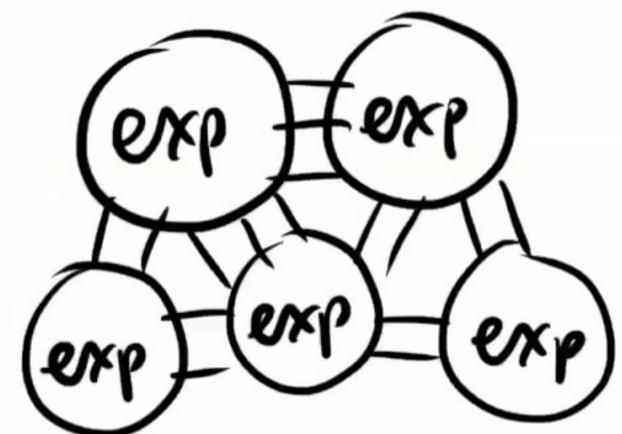
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This work: if global min-cut has polylog(n) vertices on smaller side ("unbalanced"), then can find in polylog(n) s-t max-flows

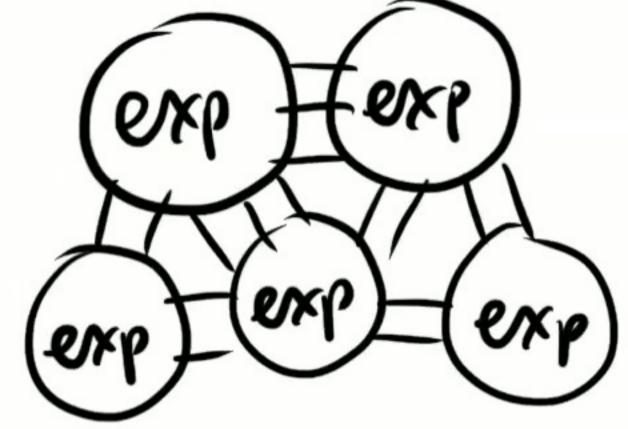
Expander Decomposition

Solve when graph is an expander (easy case)
For general graphs, decompose into expanders,
solve on each expander, and
recurse



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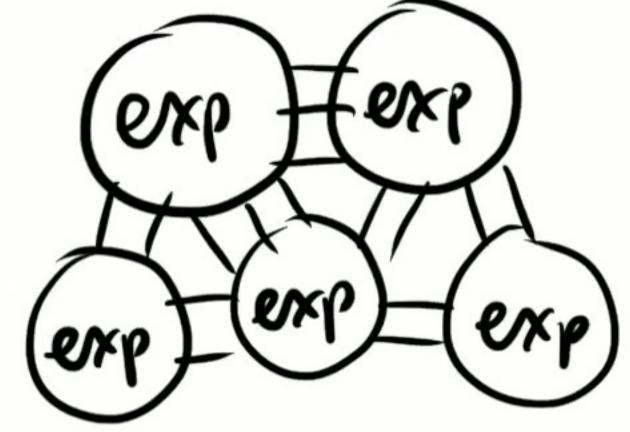
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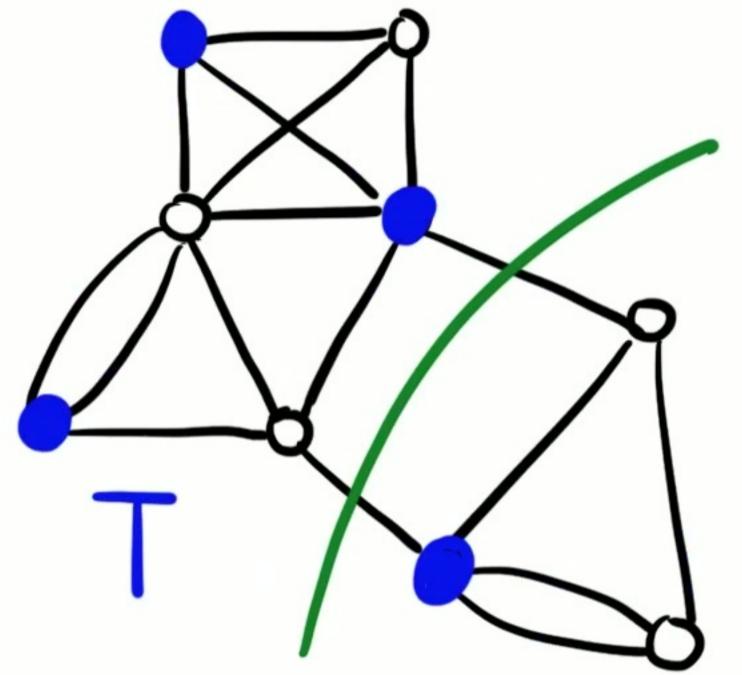
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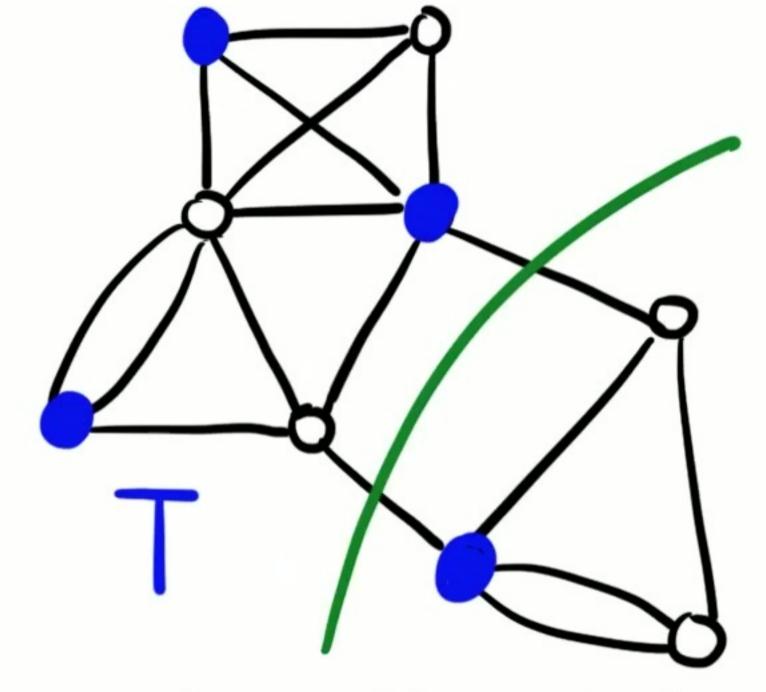
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For min-cut: when graph is an expander, the min-cut must be unbalanced! (polylog(n) vertices on smaller side.) So local algorithm works.

Suppose (S, V\S) is a min-cut. $T \subseteq V$ is an isolator if $|S \cap T| = 1$

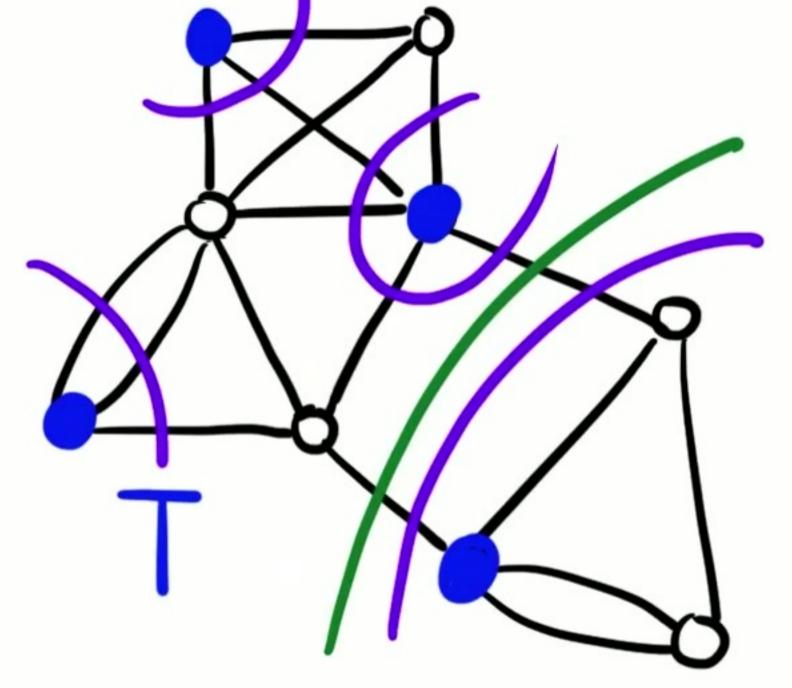


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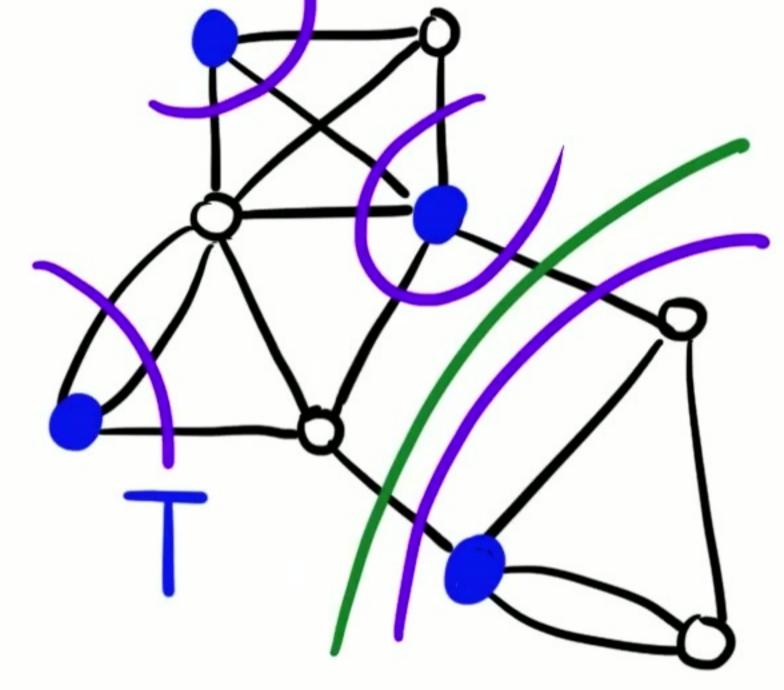
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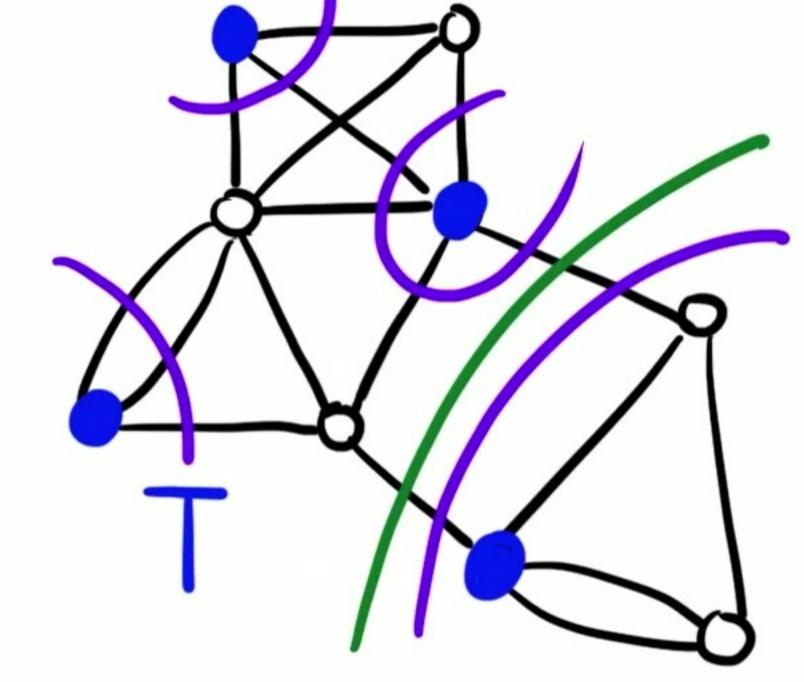
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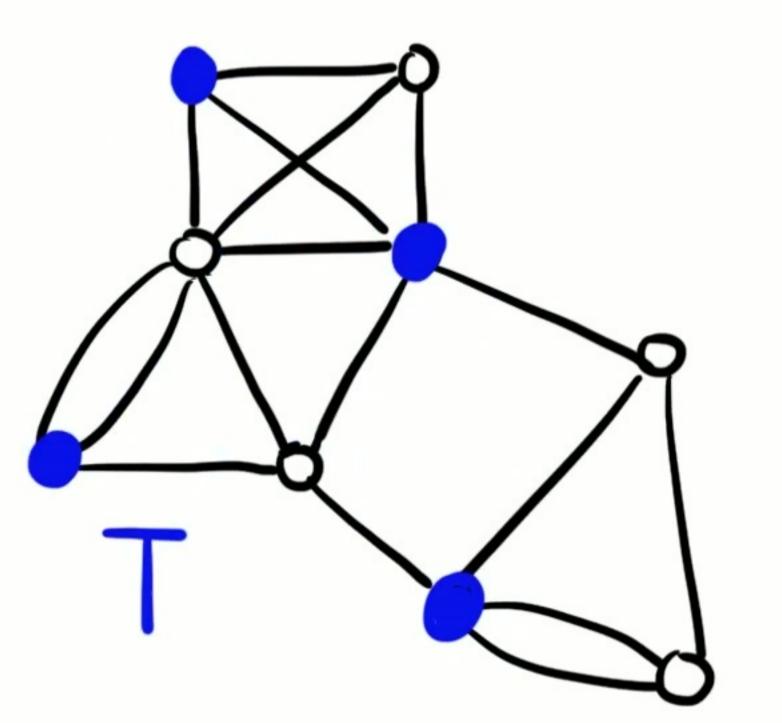
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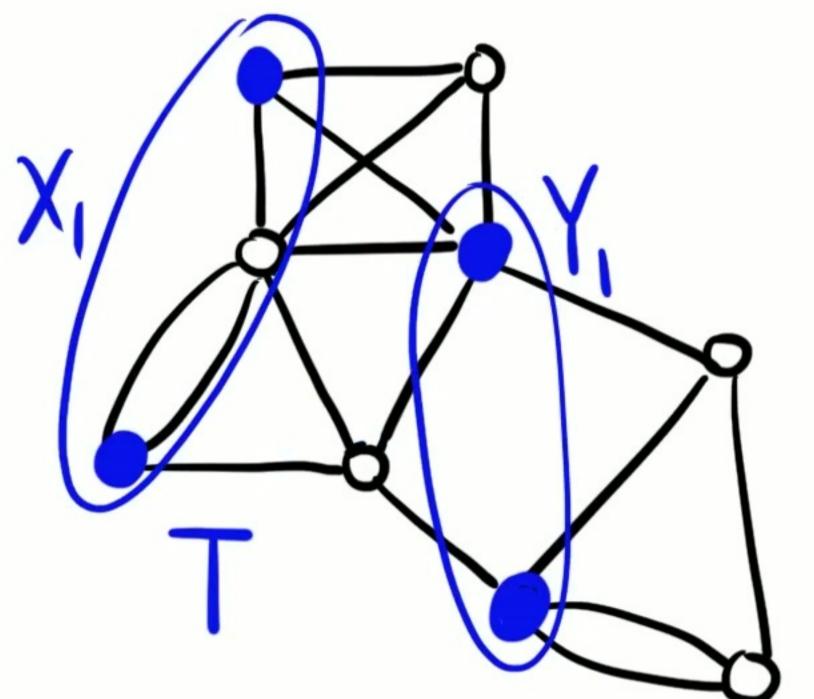


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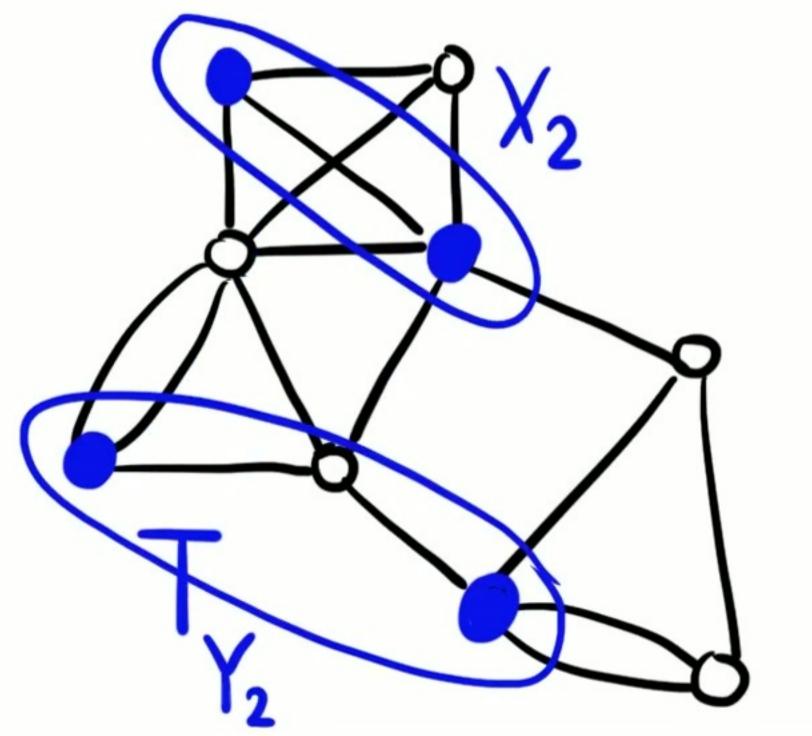
- Compute log ITI bipartitions of T, (X_k, Y_k)
 - Want: each pair s,t in T is separated in at least one of them
 - Encode vertices in T using $log_2|T|$ bits; for each bit position k, X_k = vertices with k^{th} bit 0, Y_k = vertices with k^{th} bit 1



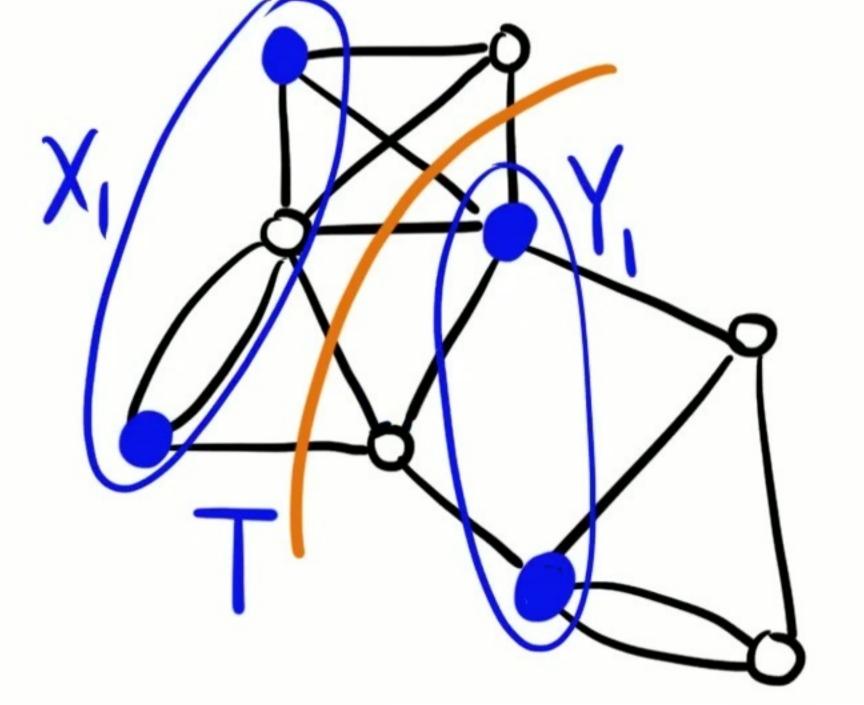
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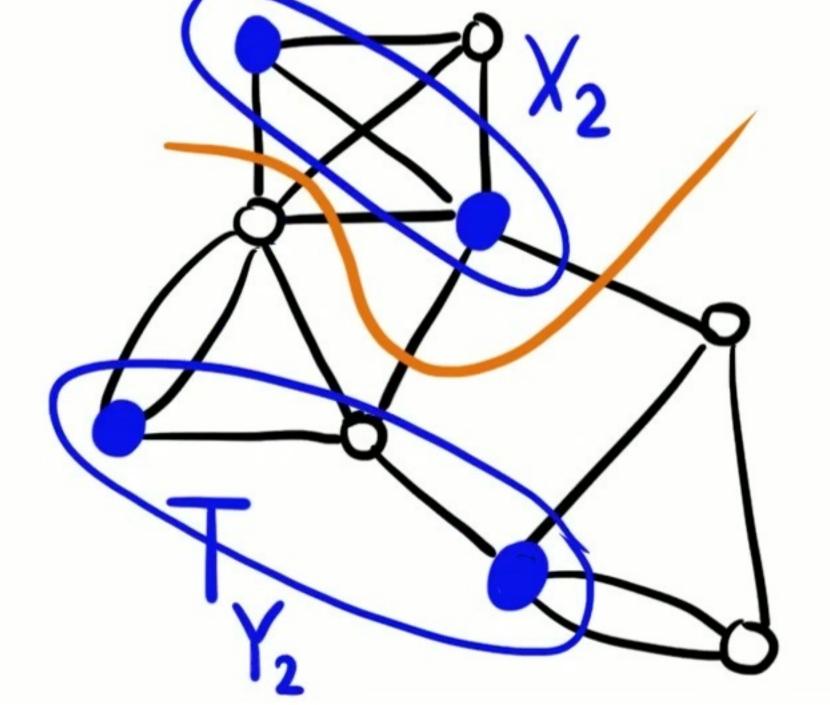


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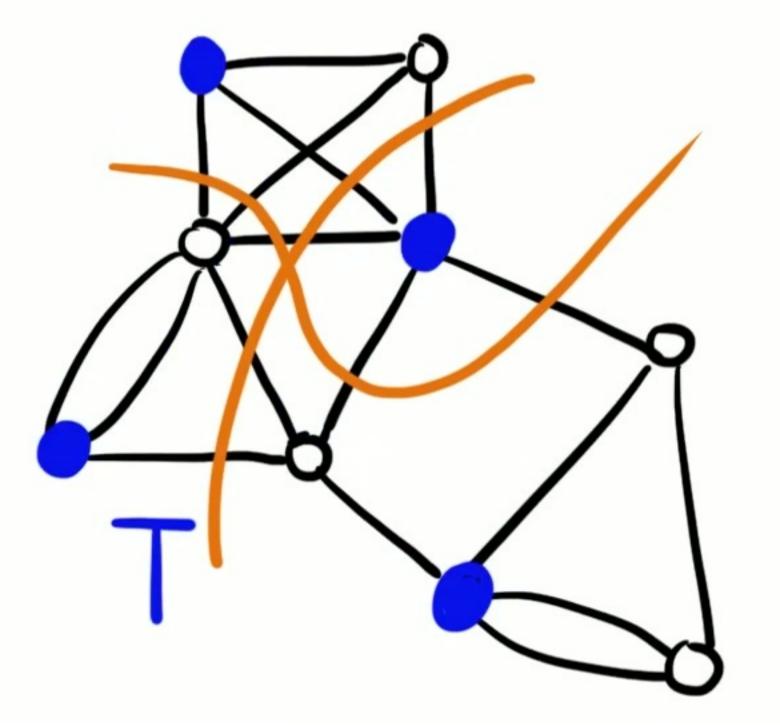


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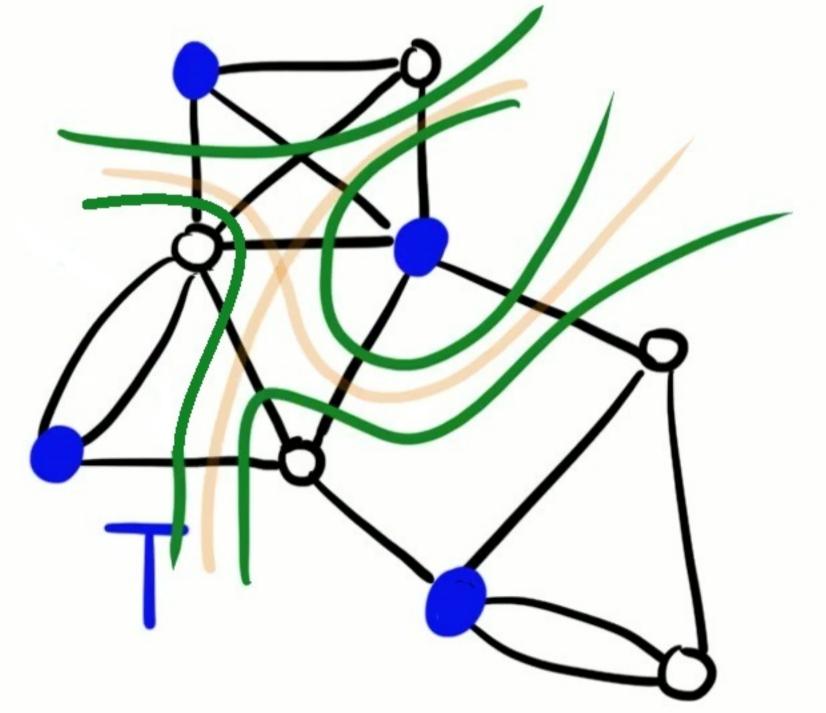
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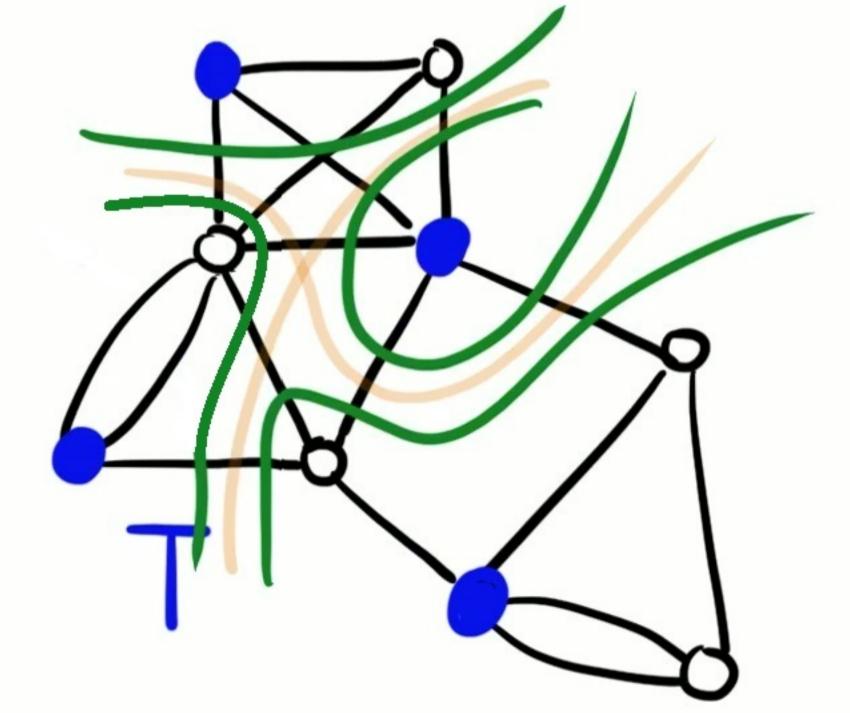
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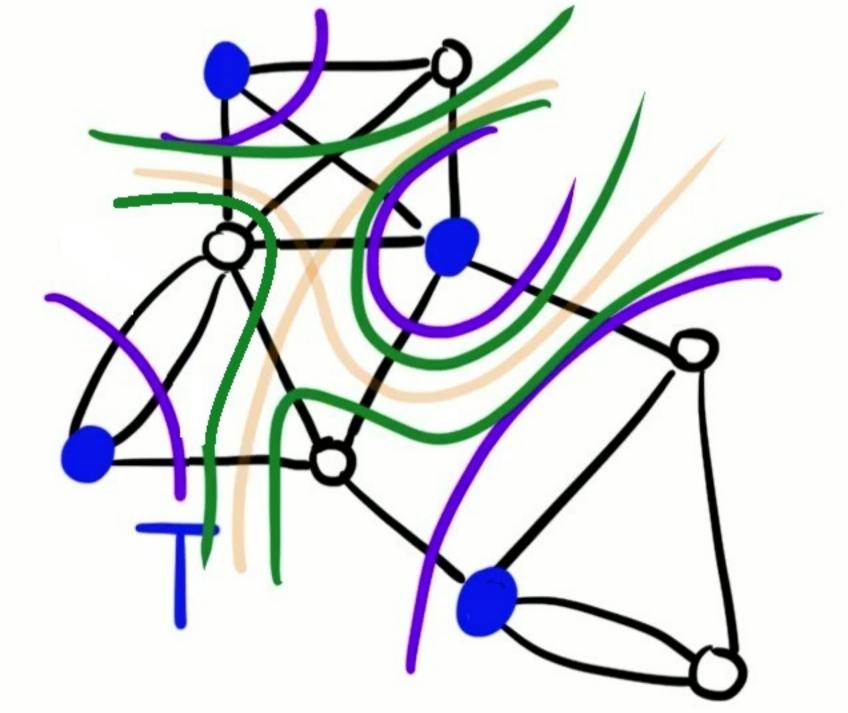
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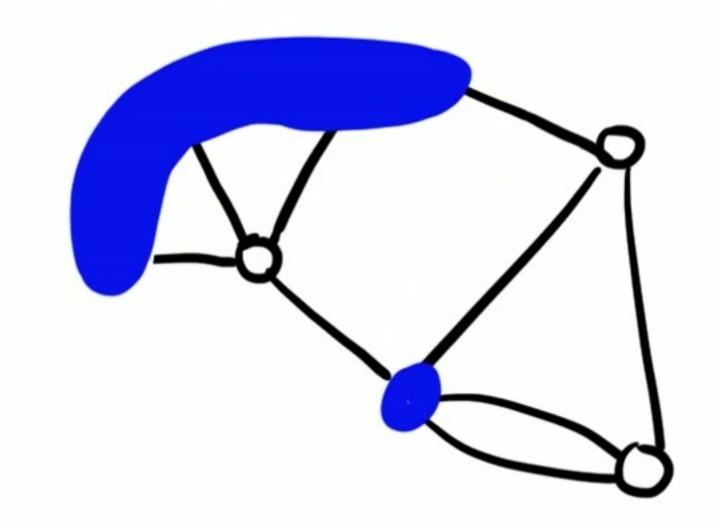


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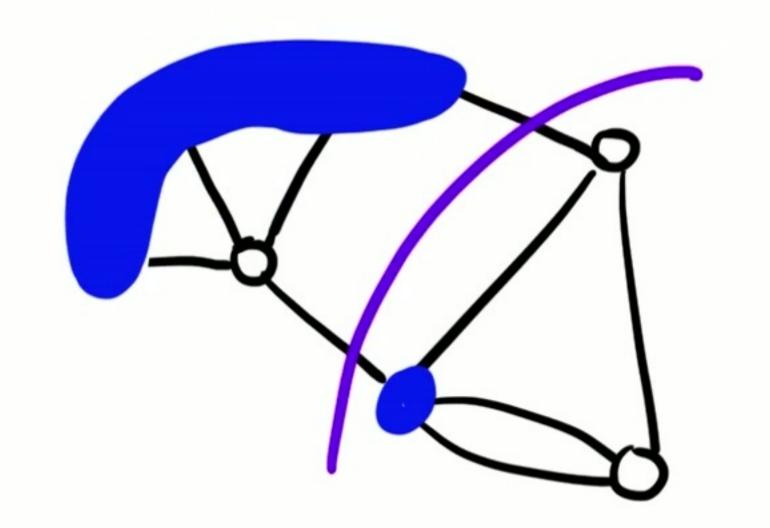


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 => sampled set T is an isolator
- Run the isolator algorithm for all i, output smallest (t, T\t)-min-cut found

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- Unbalanced case: T = V is an almost-isolator if ISI < polylog(n). Algorithm calls polylog(n) max-flows

Balanced case: det. sparsification

Suppose IS↑TI, |(V\S)↑T)|> polylog(n) [initially T=V]

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Suppose IS\capTI, |(V\setminus S)\cap T)|> polylog(n) [initially T=V]
Goal: find T' s.t. IT'I < ITI/2 (sparsification)
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Suppose
$$|S \cap T|$$
, $|(V \setminus S) \cap T)| > \text{polylog(n)}$ [initially T=V]
Goal: find T' s.t. $|T'| < |T|/2$ (sparsification)
 $|S \cap T'| > 0$, $|(V \setminus S) \cap T'| > 0$
(still hit both sides)

Algorithm: start with T=V
run unbalanced case, then sparsify T,
repeat until ITI = 1
return smallest (t, T\t)-min-cut found

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sum of degrees in S

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Why are ϕ -expanders easy? [$\phi = \frac{1}{\text{polylog(n)}}$] Claim: in a ϕ -expander, then $|S| \le 1/\phi$

sum of degrees in S

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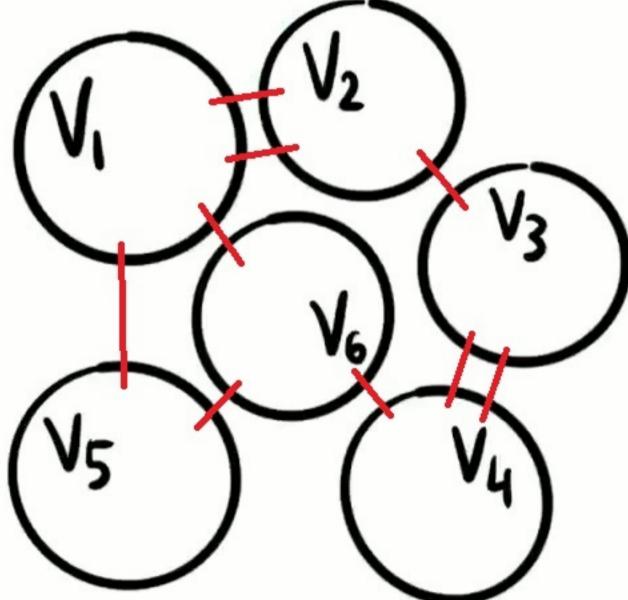
Claim: in a φ-expander, then ISI≤ 1/φ

Proof: Suppose
$$vol(s) \leq vol(v \mid s)$$

 $vol(s) = \sum_{v \in s} deg(v) \geq \sum_{v \in s} \lambda = \lambda \mid s \mid [\lambda = \min_{v \in s} -\omega t]$
 $\phi \leq \overline{\Phi}(G) \leq \frac{\mid E(s, v \mid s) \mid}{vol(s)} \leq \frac{\lambda}{\lambda \mid s \mid} \iff \mid s \mid \leq \frac{1}{\delta}$

Expander decomposition: partition V into $V_1, ..., V_k$ s.t.

- (1) Each induced graph G[V_i] is a ϕ -expander
- (2) At most half of edges go between expanders

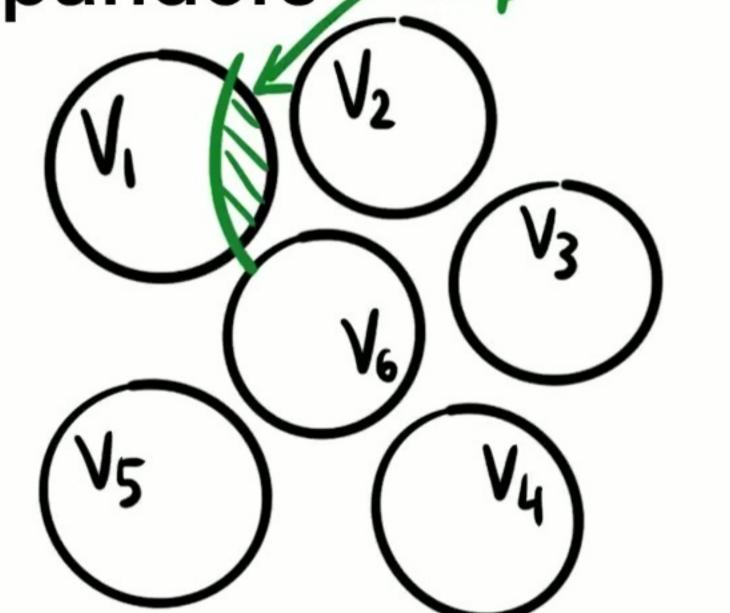


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Other applications of isolators

- Steiner min-cut in polylog(n) max-flows
- More applications?